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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1914	SERIAL NO. Priority 09/651,815
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Jerome Michael Eldridge	
				FILING DATE Priority August 30, 2000	GROUP Priority 2813

U.S. PATENT DOCUMENTS							
*Examiner Initial	Class	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE JLE </div>	AA	3,954,523	05/76	Magdo et. al.			
	AB	5,023,200	06/91	Blewer et al.			
	AC	5,470,801	11/95	Kapoor et al.			
	AD	5,885,900	03/99	Schwartz			
	AE	5,731,241	03/98	Jang et al.			
	AF	5,783,253	07/98	Roh			
AG							

FOREIGN PATENT DOCUMENTS								
*Examiner Initial	Class	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>		AL						
		AM						
		AN						
		AO						
		AP						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>	AQ		Togo, M., "A Gate-side Air-gap Structure (GAS) to Reduce the Parasitic Capacitance in MOSFETs", 1996 Sympos. on VLSI Technology
			Digest of Technical Papers, IEEE 1996, pp. 38-39
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>	AR		Anand, M.B., "NURA: A Feasible, Gas-Dielectric Interconnect Process", 1996 Symposium on VLSI Technology Digest of Technical
			Papers, IEEE 1996, pp. 82-83
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>	AS		Watanabe, H., "A Novel Stacked Capacitor with Porous-Si Electrodes for High Density DRAMs", Microelectronics Research
			Laboratories, NEC Corp., date unknown, pp. 17-18
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>	AT		ABSTRACT: Anderson, R.C. et. al., "Porous Polycrystalline Silicon: A New Material For MEMS", Jnl. of Microelectromechanical
			Systems (Mar. 1994), Vol. 3, No. 1, pp. 10-18
<div style="font-size: 2em; font-family: cursive;"> JLE JLE JLE JLE JLE </div>	AU		M. B. Anand, "Use of Gas as Low-k Interlayer Dielectric in LSI's: Demonstration of Feasibility", IEEE Transactions On Electron
			Devices, Vol. 44, No. 11, November 1997, pp. 1965-1971

EXAMINER <div style="font-size: 2em; font-family: cursive;">Hanna Schut</div>	DATE CONSIDERED <div style="font-size: 2em; font-family: cursive;">5/3/03</div>
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Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
M122-1914SERIAL NO.
Priority 09/651,815LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)APPLICANT
Jerome Michael EldridgeFILING DATE
Priority August 30, 2000GROUP
Priority 2813

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>He</i>	AA	U.S. Patent Application Serial No. 08/947,847		Juengling et al.			10/9/97
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

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	AO							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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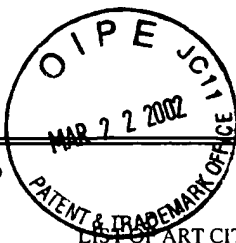
EXAMINER

Harold S. Seltzer

DATE CONSIDERED

5/3/03

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1914		SERIAL NO. 10/044,206		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Jerome Michael Eldridge				
				FILING DATE January 11, 2002		GROUP 2813		
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,273,616	12/1993	Bozler	156	603		
	AB	6,140,200	10/2000	Eldridge	438	396		
	AC	5,989,776	11/1999	Felter et al.	430	270		
	AD							
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EXAMINER 				DATE CONSIDERED 5/3/05				
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